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SECOND SUPPLEMENTAL INFORMATION
PISCLOSURE STATEMENT
BY APPLICANTS

TRADELLE 1 of 2

Complete If Known			
Application Number	10/579,682		
Filing Date	November 19, 2004		
First Name Inventor	Reuven Bakalash et al.		
Group Art Unit	2628		
Examiner Name	Hau H. Nguyen		
Attorney Docket Number	142-002USAC00		

U.S. PATENT DOCUMENTS							
Examiner Initials	Cite No.	U.S. Patent Documents		Name of	Date of Publication of		
		Number	Kind Code (if known)	Patentee or Applicant of Cited Document	Cited Document MM-DD-YYYY	Intn'l Class / Sub Class	
		7,598,958 B1		Kelleher	10/06/2009	G06F 15/16	
		7,525,547 B1		Diard	04/28/2009	G06F 15/16	
		7,388,581 B1	-	Diard et al.	06/17/208	G06T 15/40	
		6,956,579 B1		Diard et al.	10/18/2005	G06F 13/00	
		6,362,825 B1		Johnson	03/26/2002	G06T 15/00	

Receipt date: 03/08/2010



TRADENIA	PUBLICATIONS					
Examiner Initials	Cite No.	Description				
		Antonio Garcia and Han-Wei Shen "An Interleaved Parallel Volume Render With PC-clusters", Proceedings of the Fourth Eurographics Workshop on Parallel Graphics and Visualization, 19 pages, 2002.				
,		Erik Reinhard and Chuck Hansen, "A Comparison of Parallel Compositing Techniques on Shared Memory Architectures", Accepted for the Eurographics Workshop on Parallel Graphics and Visualisation, Girona, Spain, 9 pages, September 2000.				
		Steven Molnar, "Combining Z-buffer Engines for Higher-Speed Rendering", Proceedings of the 1988 Eurographics Workshop on Graphics Hardware, 11 pages, 1988.				
		John Eyles et al., "PixelFlow: The Realization," SIGGRAPH/EUROGRAPHICS Conference on Graphics Hardware, 13 pages, 1997.				

EXAMINER

/Hau Nguyen/

DATE CONSIDERED

04/25/2010

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance or not considered. Include copy of this form with next communication to applicant.

(INFORMATION DISCLOSURE STATEMENT - SECTION 9 PTO-1449)